

Fig. 1a

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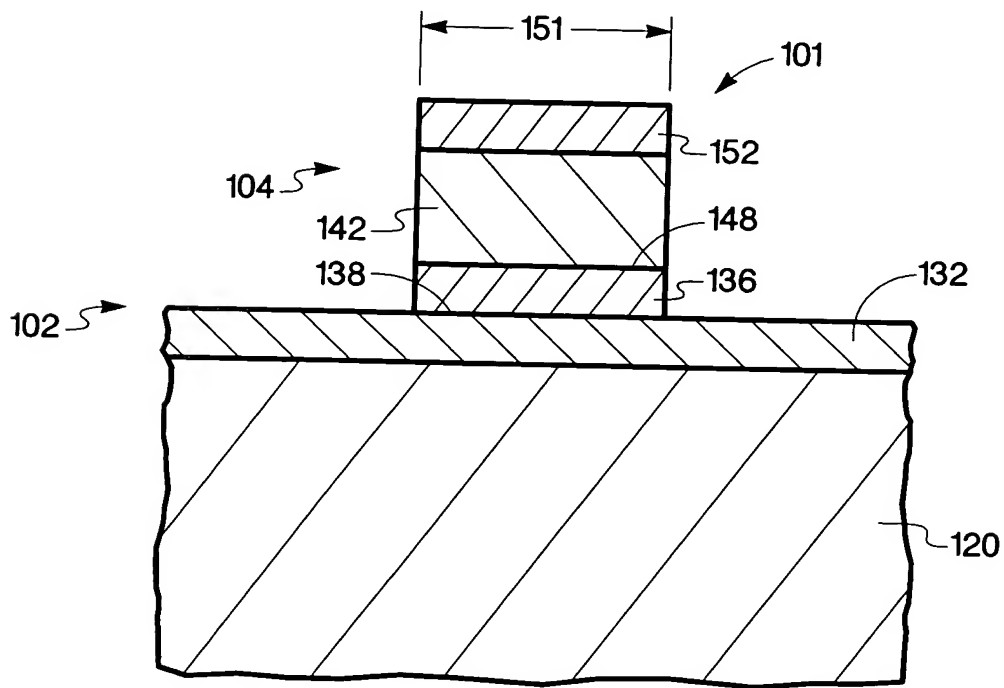


Fig. 1b

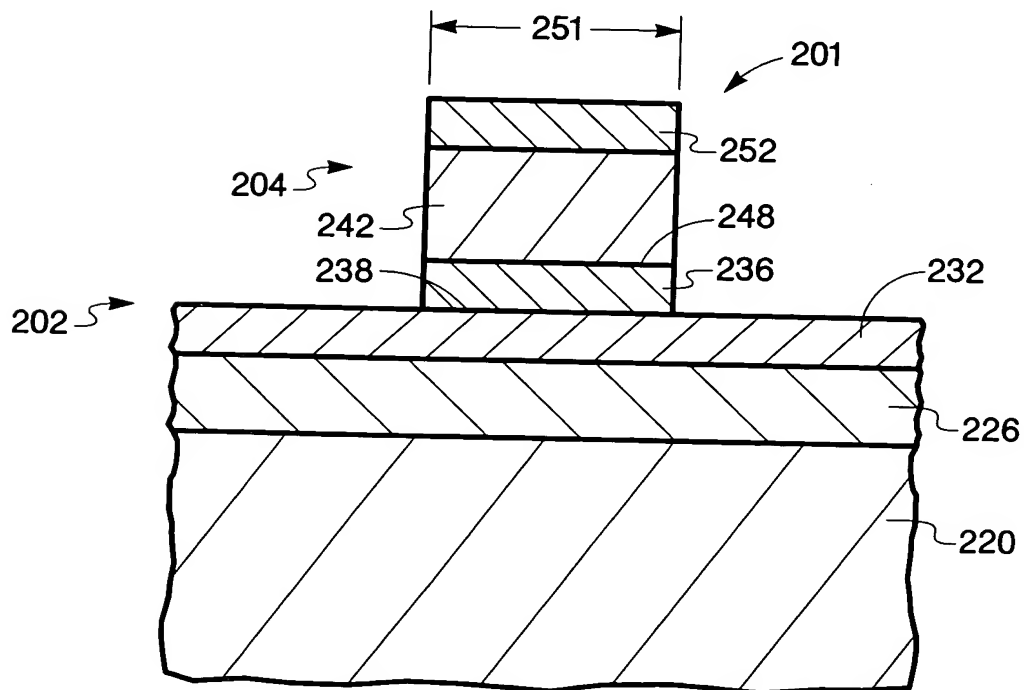


Fig. 2

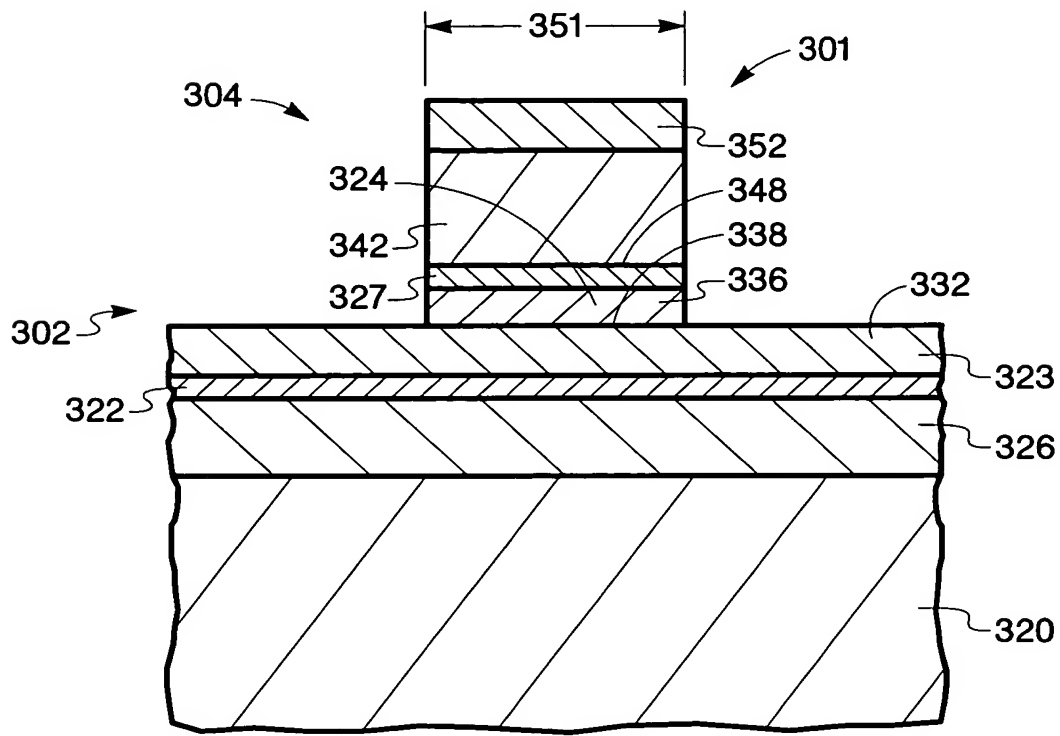


Fig. 3

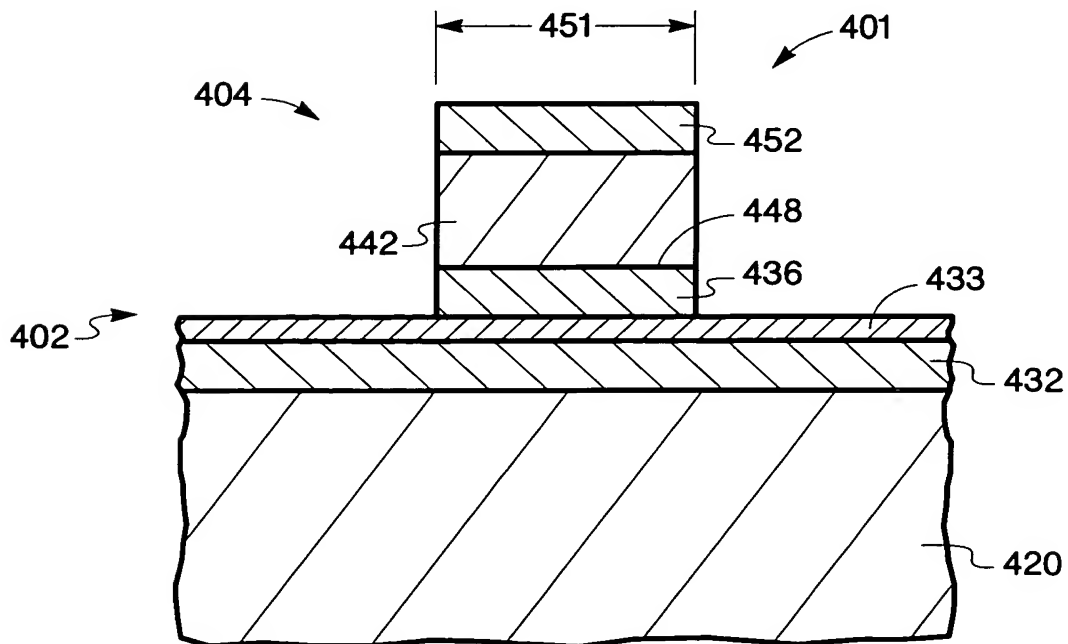


Fig. 4

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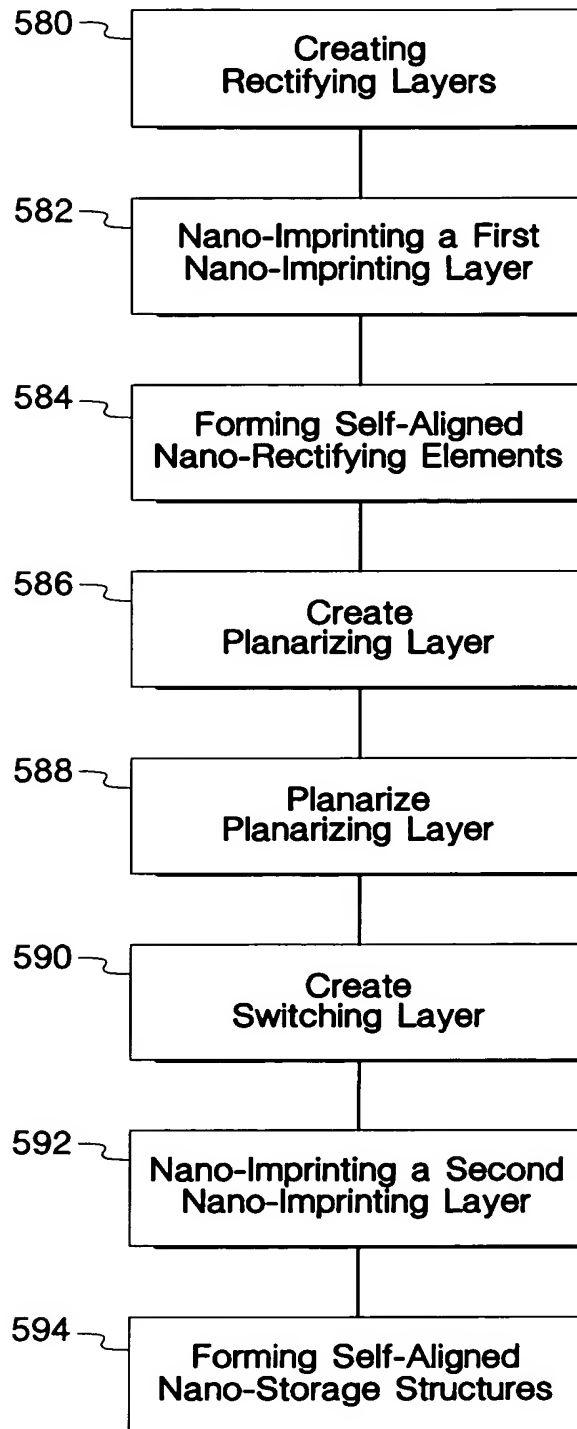


Fig. 5

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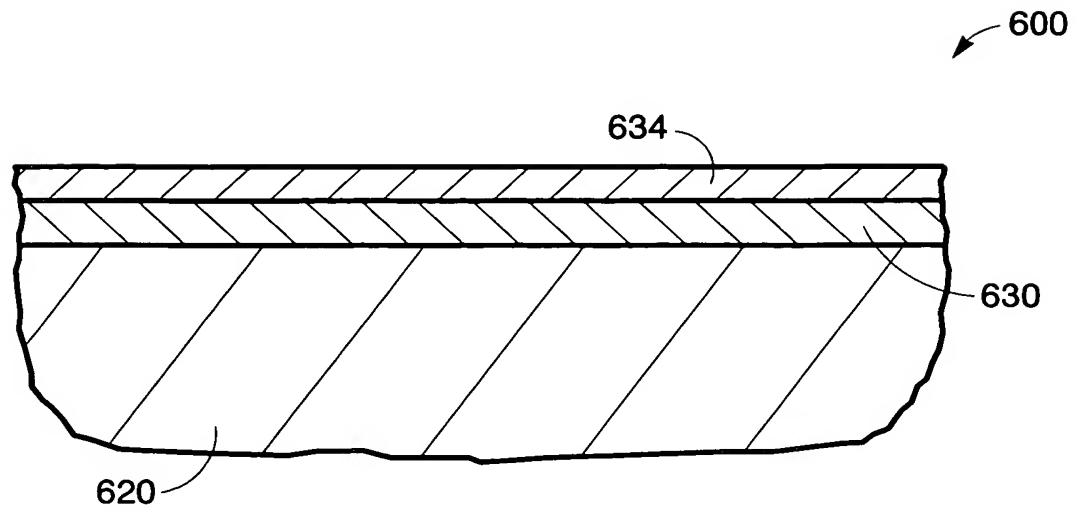


Fig. 6a

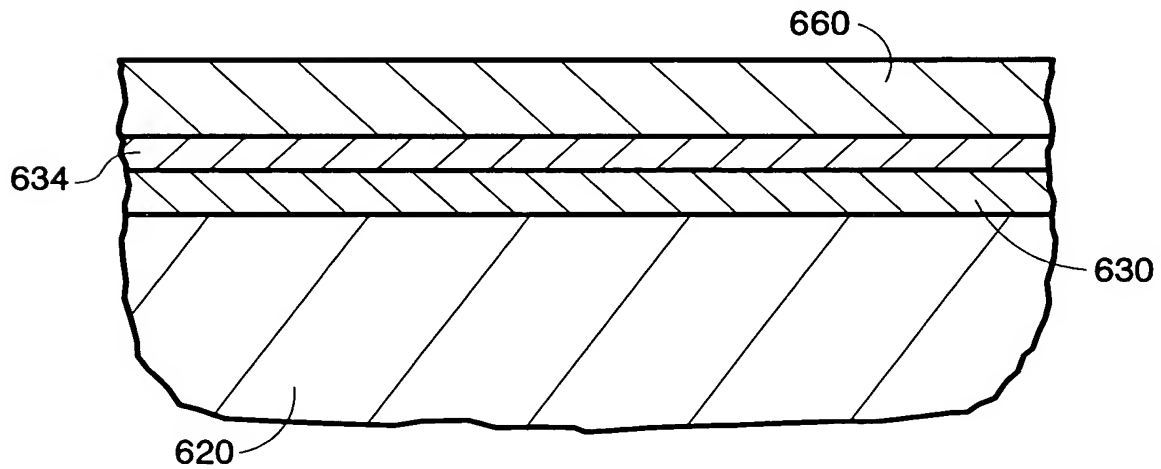


Fig. 6b

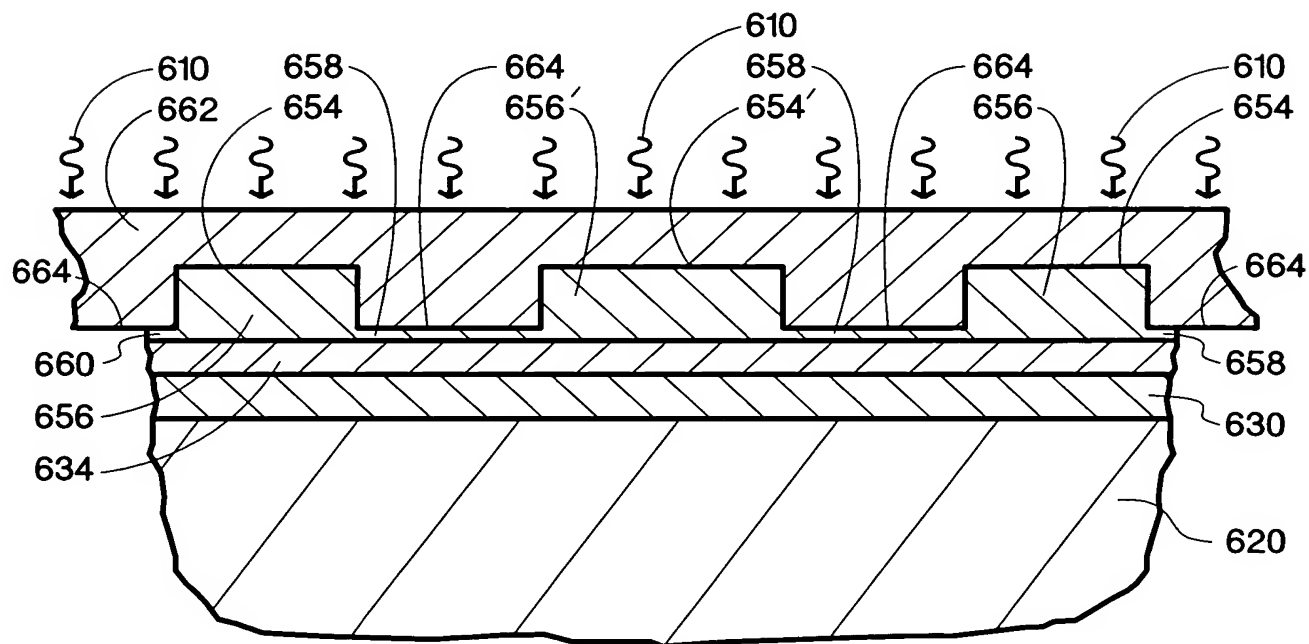


Fig. 6c

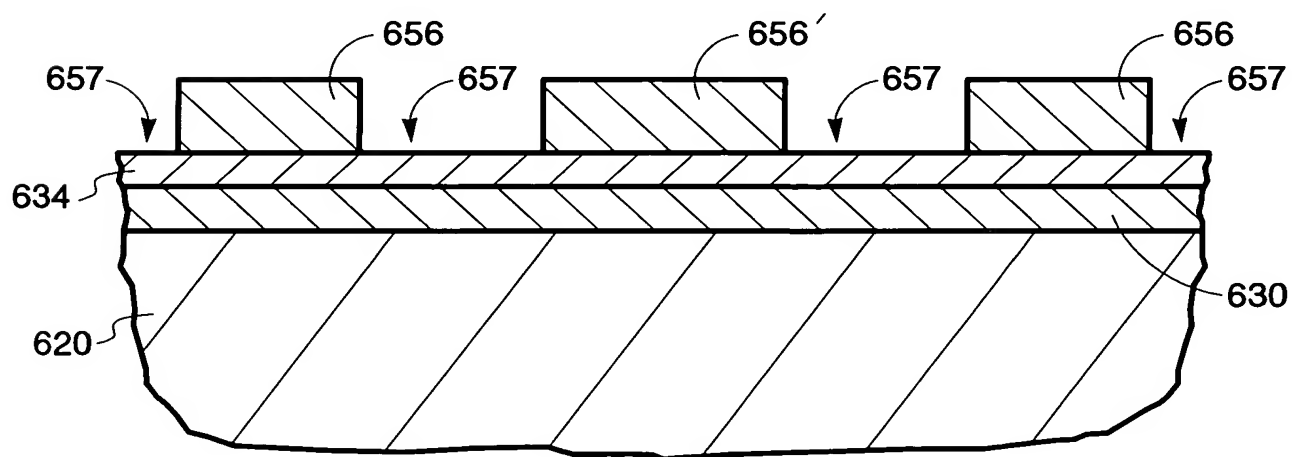


Fig. 6d

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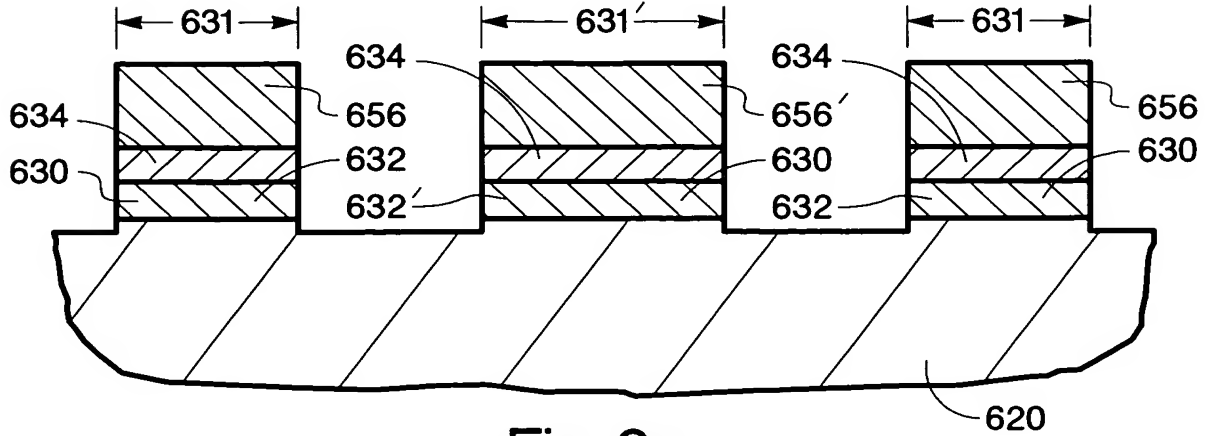


Fig. 6e

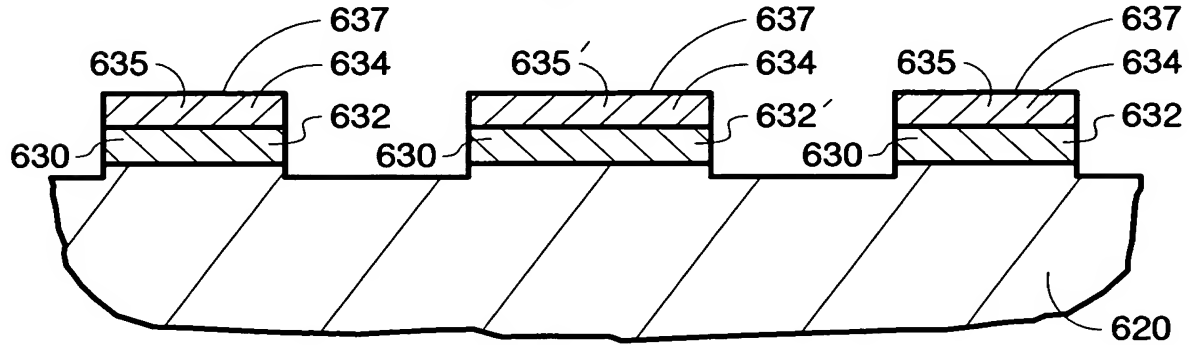


Fig. 6f

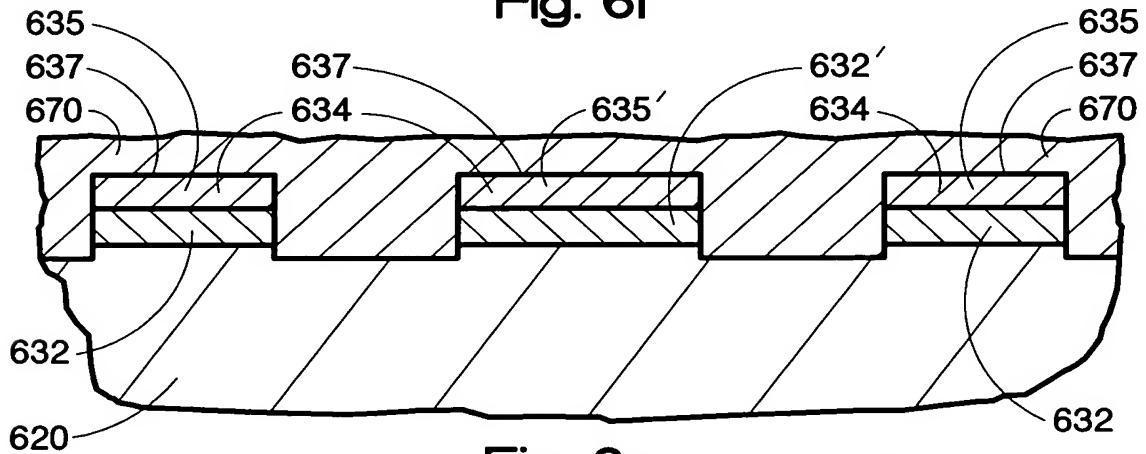


Fig. 6g

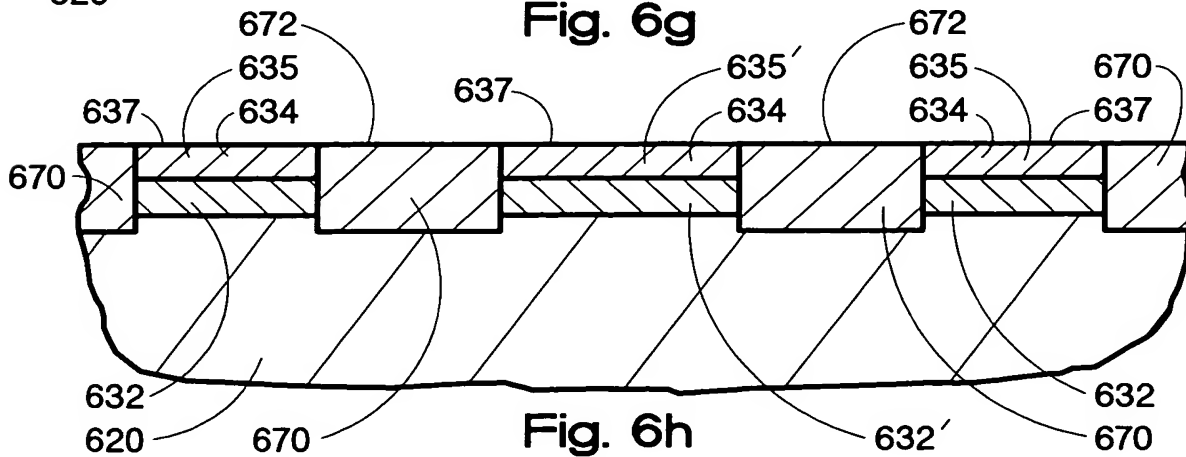


Fig. 6h

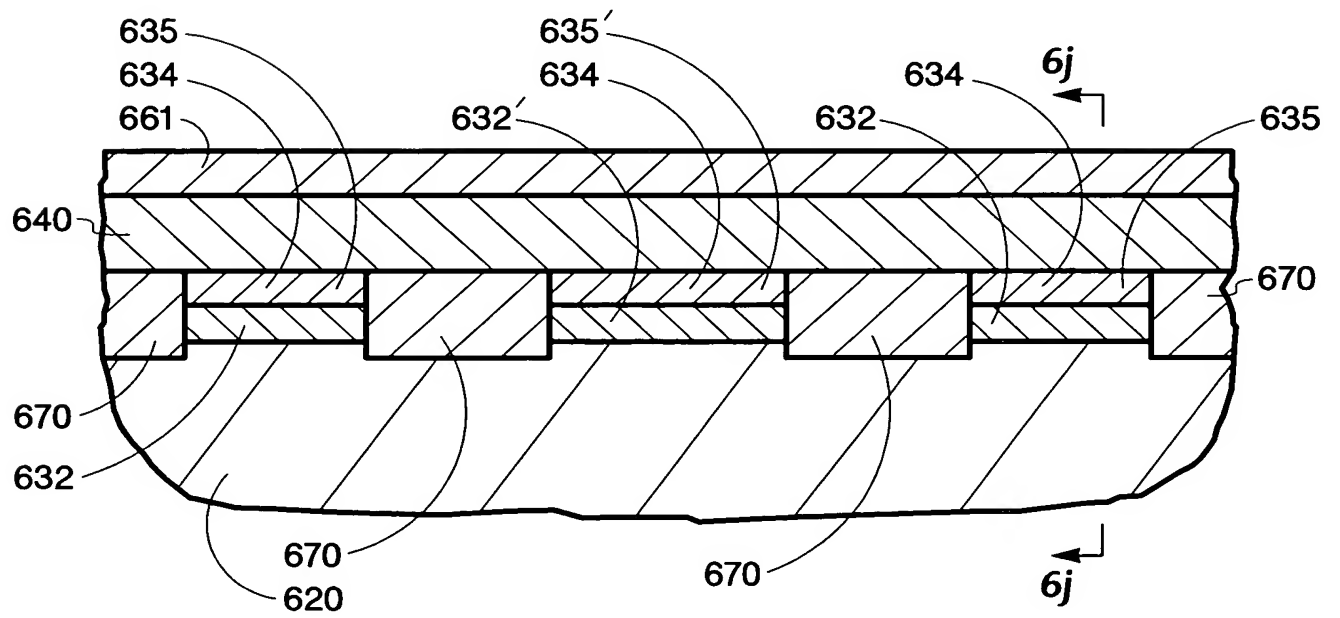


Fig. 6i

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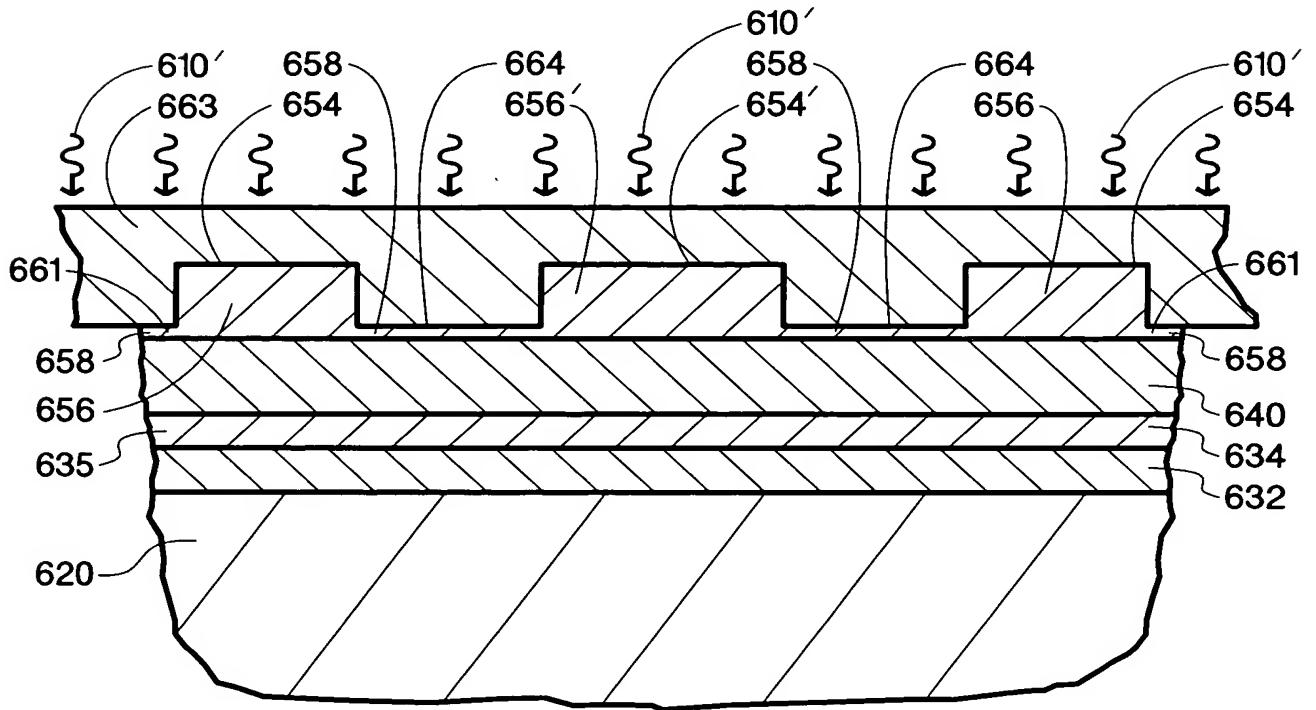


Fig. 6j

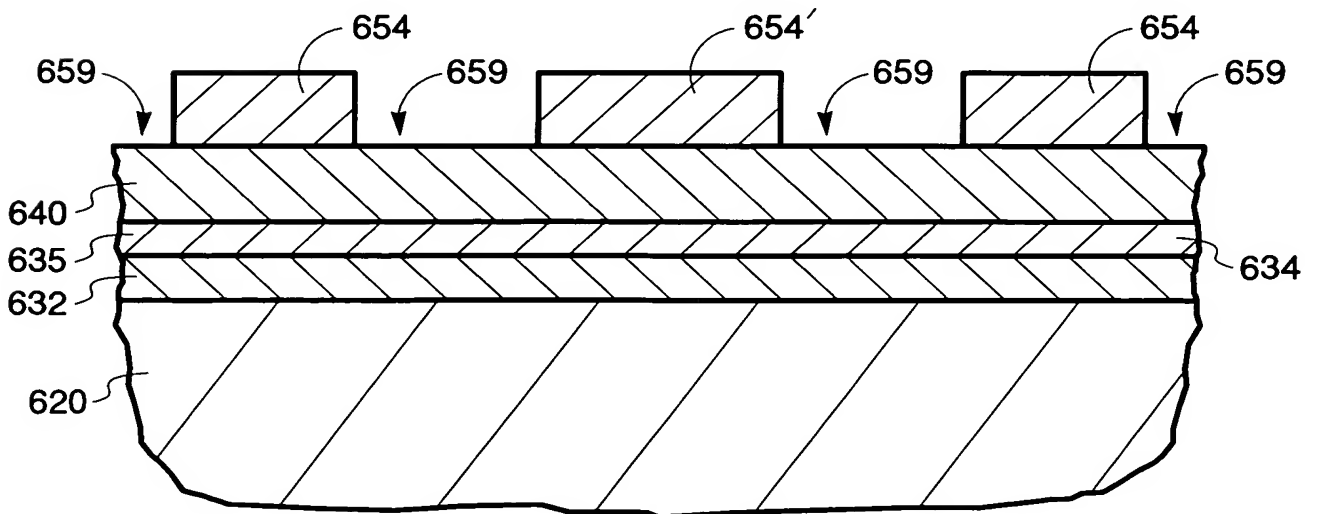


Fig. 6k

NANOMETER-SCALE MEMORY DEVICE UTILIZING
SELF-ALIGNED RECTIFYING ELEMENTS AND METHOD OF MAKING

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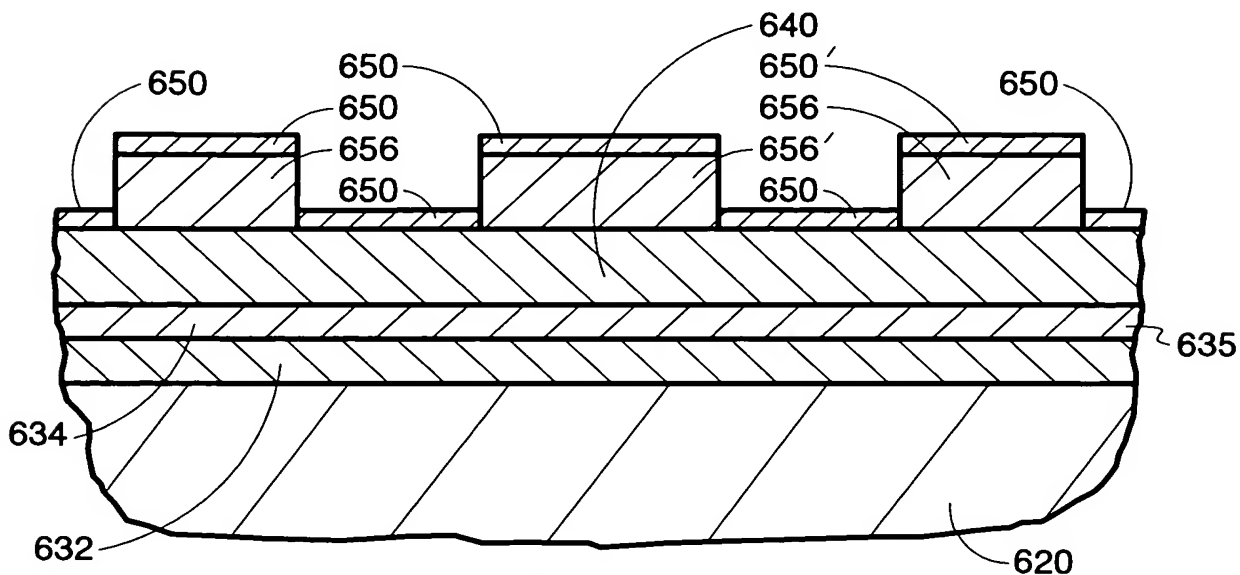


Fig. 6l

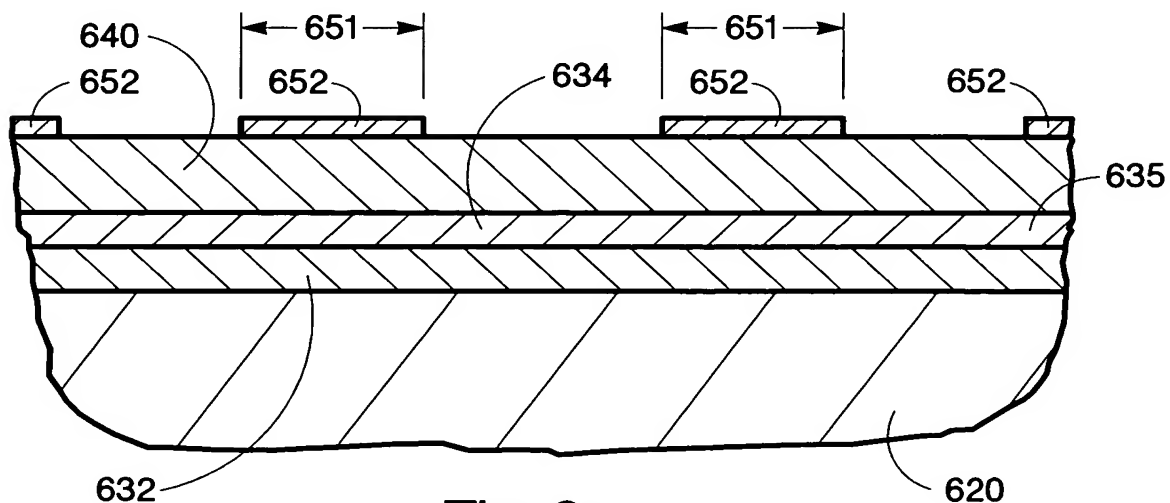


Fig. 6m

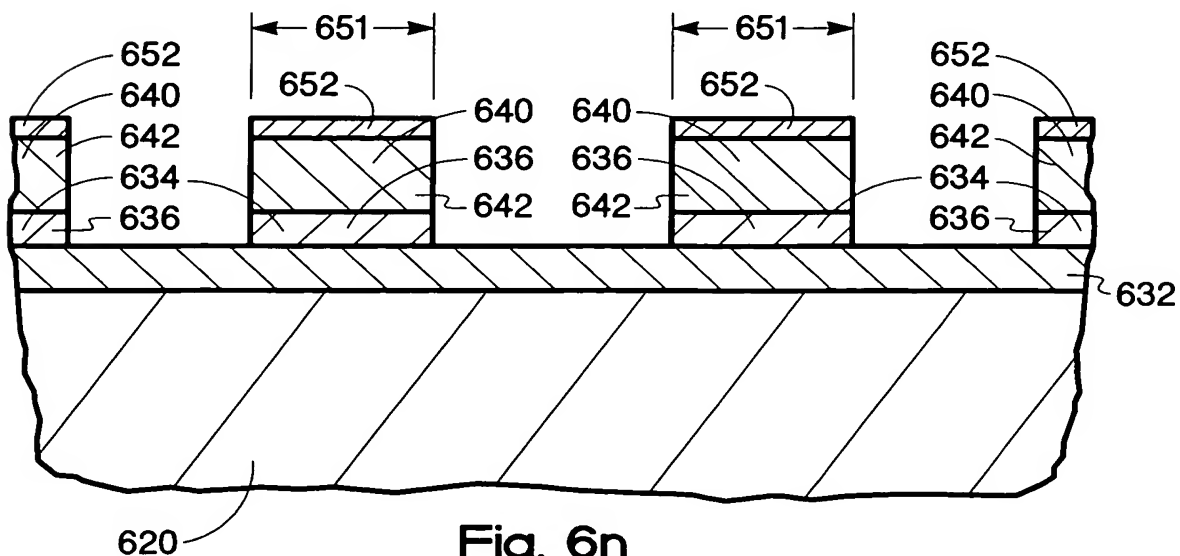


Fig. 6n

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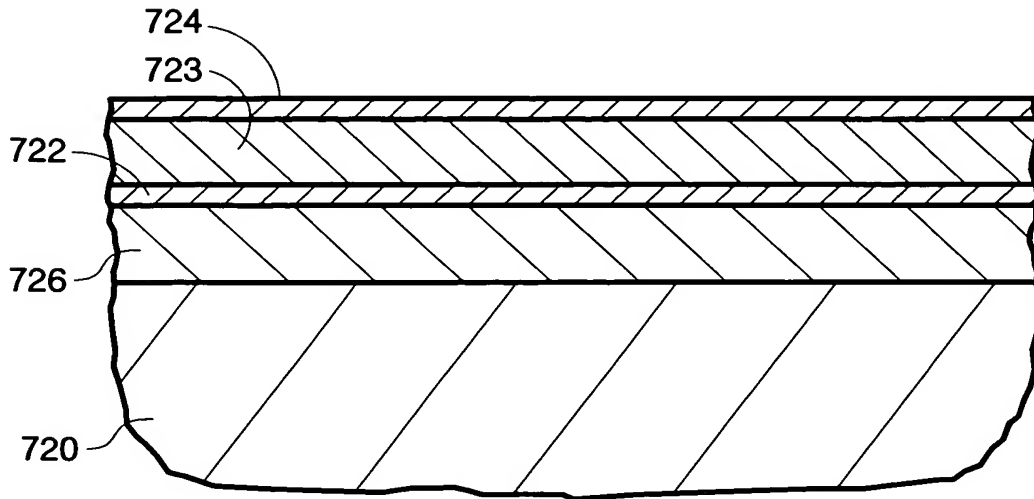


Fig. 7a

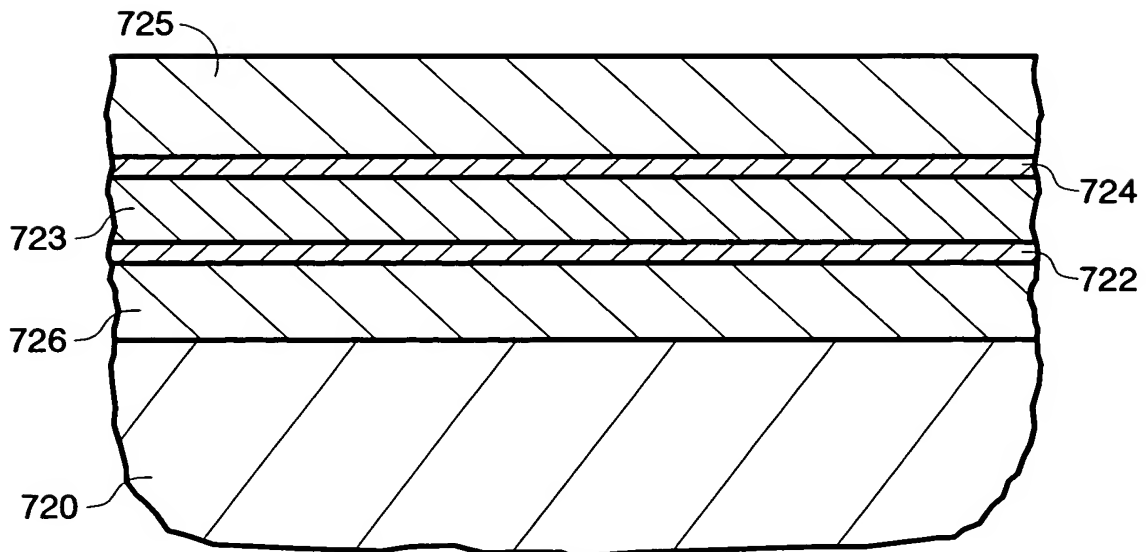


Fig. 7b

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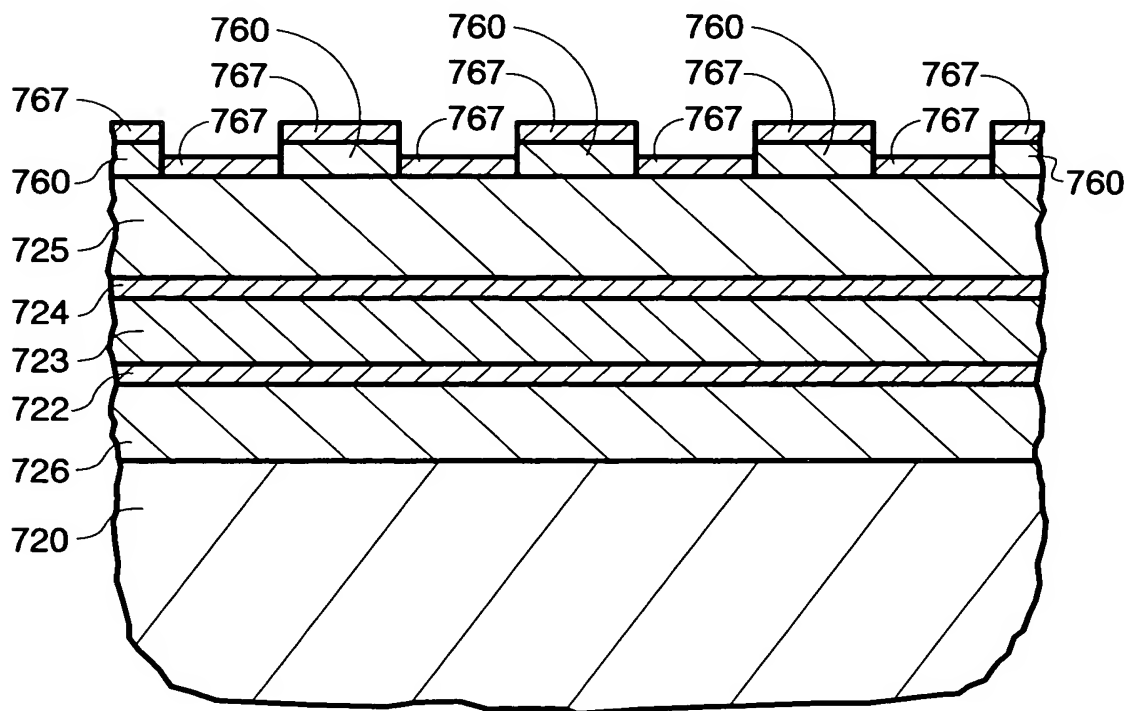


Fig. 7c

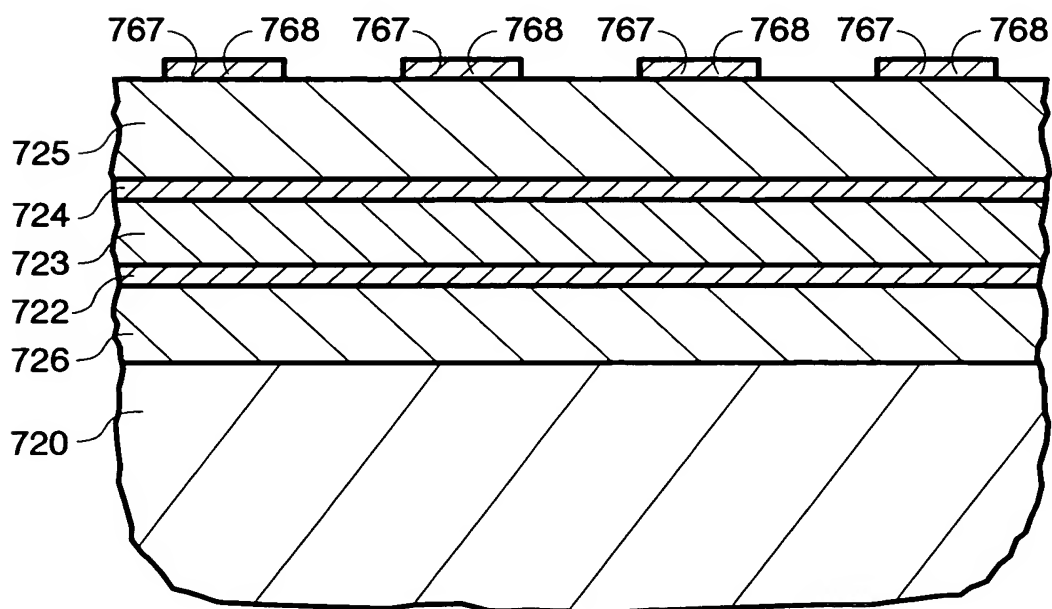


Fig. 7d

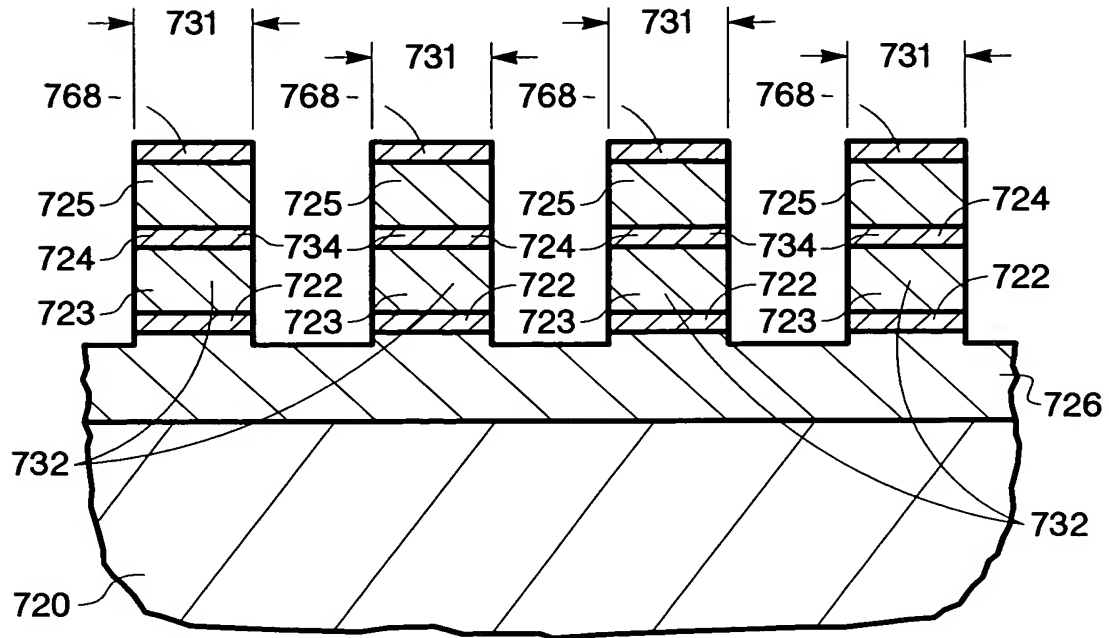


Fig. 7e

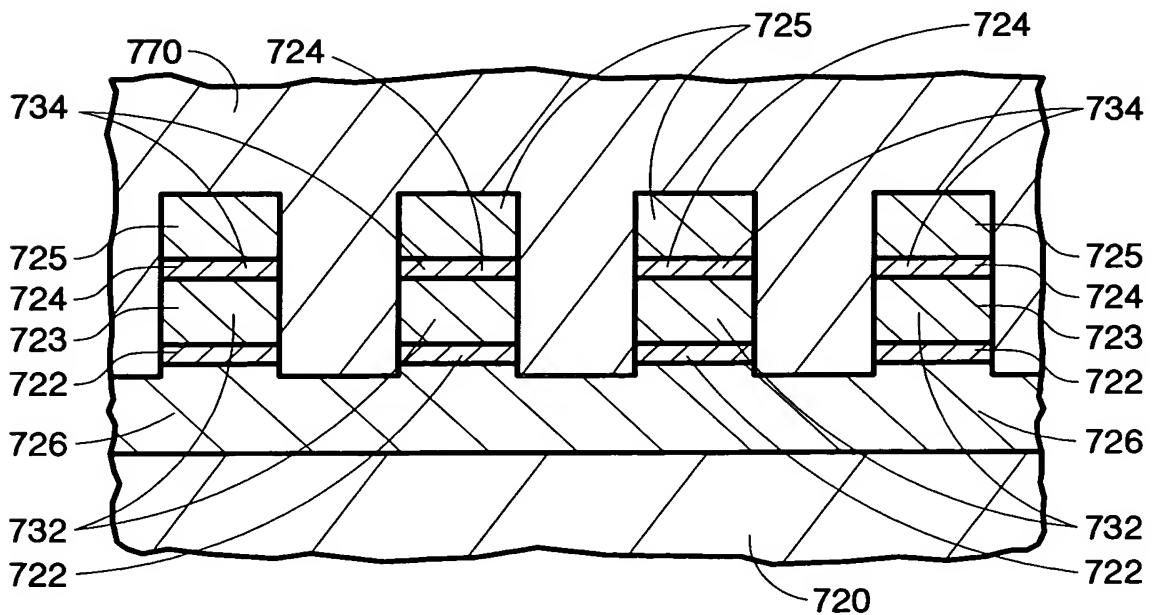


Fig. 7f

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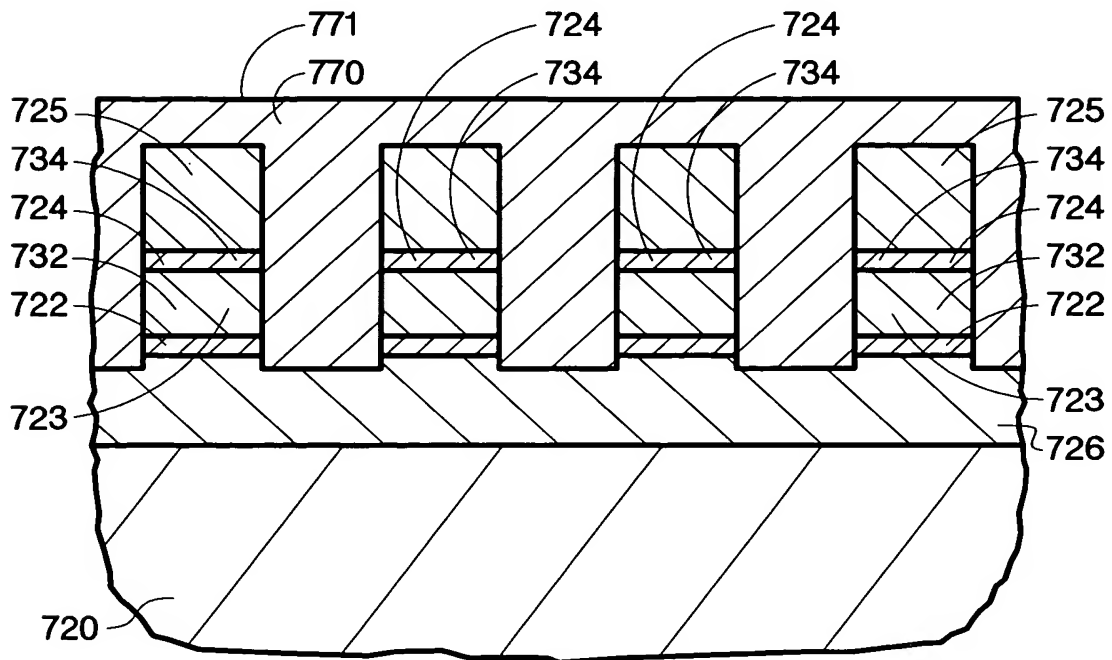


Fig. 7g

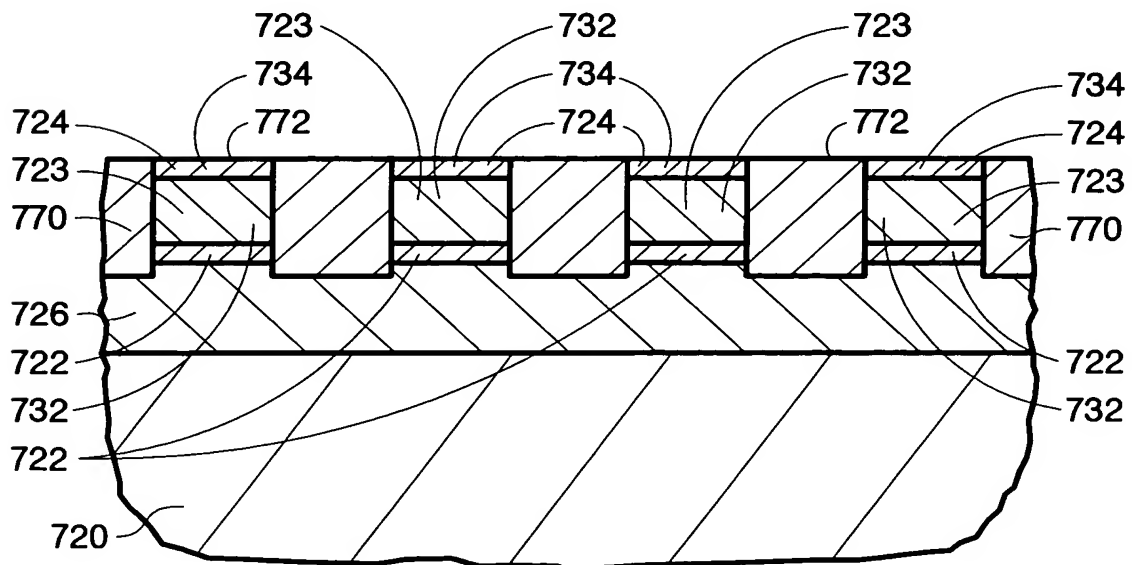


Fig. 7h